IR

PATENT



Docket No.: 56937-089

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Kenichiro UDA : Confirmation Number: 3242

Application No.: 10/658,195 : Group Art Unit: 2818

Filed: September 10, 2003 : Examiner: Unknown

For: STRUCTURE OF POWER SUPPLY PATH UTILIZED IN DESIGN OF INTEGRATED

CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Mail Stop AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The Japanese reference, 03-250735, was first cited in a corresponding Japanese application office action and its relevance discussed therein. A copy of the foreign office action is attached for the Examiner's information.

10/658,195

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOPT WILL & EMERY LLP

Michael E. Fogarty Registration No. 36,139

Please recognize our Customer No. 20277 as our correspondence address.

600 13th Street, N.W. Washington, DC 20005-3096 Phone: 202.756.8000 MEF:tlg

Facsimile: 202.756.8087 **Date: January, 2005**

SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION					ATTY. DOCKET NO. 56937-089		SERIAL NO. 10/658,195			
					APPLICANT Kenichiro UDA	l e e e e e e e e e e e e e e e e e e e				
(PTO-1449)					FILING DATE September 10, 20		ROUP 818			
				U.S. PATE	NT DOCUMENTS	· · · · · · · · · · · · · · · · · · ·				
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code2 (# known)		Publication Date MM-DD-YYYY	Name of Patentee or App Document	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
	<u> </u>	US								
		US								
		US					_			
	ļ	US	·							
	-	US					<u> </u>			
· · · · · · · · · · · · · · · · · · ·	 	US								
		us								
		US				"				
		US					<u> </u>			
		US								
		US								
		US	77.14							
		US		FOREIGN PA	ATENT DOCUMENTS					
EXAMINER'S	1	Fo	reign Patent Document		I Pages Col	umns, Lines	Translation			
INITIALS	CITE NO.	ITE Country Codes - Number 4 - Kind		Publication Date MM-DD-YYYY		Where Relevant Figures Appear		Yes	No	
		JP 03-250735		11/8/1991	FUJITSU LTD FUJITSU VLSI LTD			w/English abstract		
		ļ								
		₩				<u> </u>			igwdapprox	
	 	-				<u> </u>	·		├ -	
	<u>. </u>		OTHER A	l RT (Including Auti	nor, Title, Date, Pertinent Pages,	Etc.)		<u> </u>		
EXAMINER'S INITIALS INITIALS INCIDENTIALS IN								ok, magazine, y where	-	
		 	· · · · · · · · · · · · · · · · · · ·	-						
EYAMINED						DATE CONSIDERED				
EXAMINER						DATE CONSIDERED				

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.